

# Modular Infrastructure for Rapid Flight Software Development

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#### **Overview**

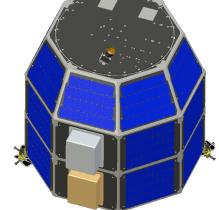


- Background
- Flight Software Development Process
- Simulink Model Overview
- Integration with cFE



#### **Background**

- Small Spacecraft Investigation
  - Modular CommonBus Spacecraft
- Hover Test Vehicle (HTV) Development
- Next Step Lunar Atmosphere and Dust Environment Experiment (LADEE)
  - Joint ARC/GSFC Mission
  - Lunar Orbiter, Launch 2012





## Flight Software Infrastructure Development

- Model Based Approach for Application Unique Software
- Latest Developments
  - Mathworks Simulink/RTW Embedded Coder
  - Integration of GSFC ITOS GDS Tool
  - Integration of GSFC Core Flight Executive (cFE)
  - Demonstrated on HTV



# **Hover Test**

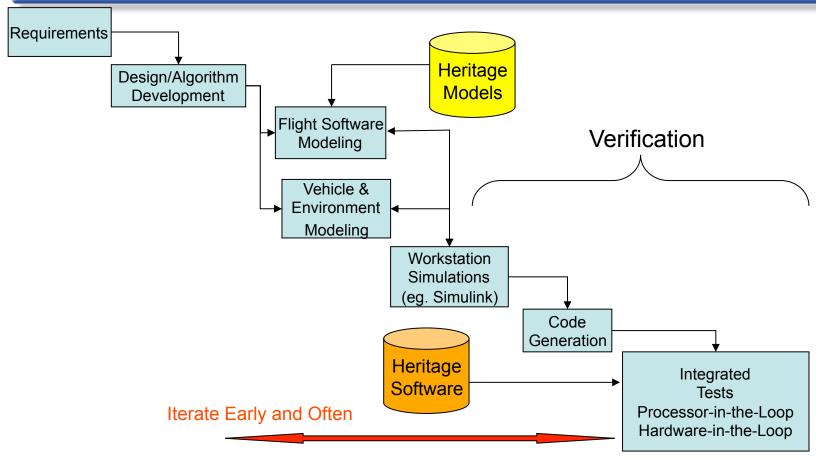




# Flight Software Development Process Overview



#### **FSW Process Overview**

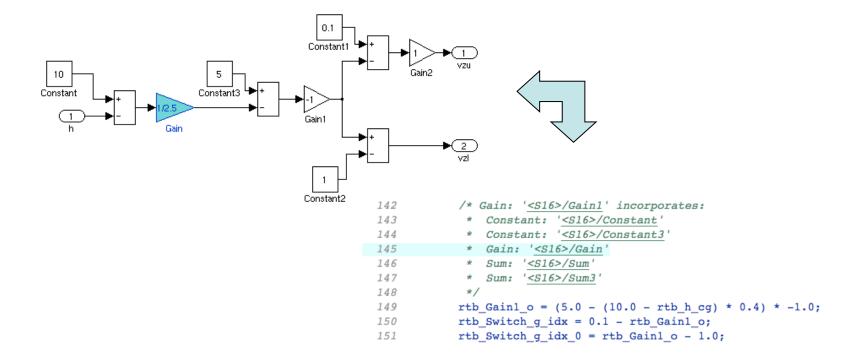


- Model Based Development Approach
  - Develop Models of FSW, Vehicle, and Environment in Simulink
  - Automatically generate Software using RTW/EC.
  - Integrate with hand-written and heritage software.
  - Iterate while increasing fidelity of tests Workstation Sim (WSIM), Processor-In-The-Loop (PIL), Hardware-in-the-Loop (HIL)

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#### **Automatic Code Generation**



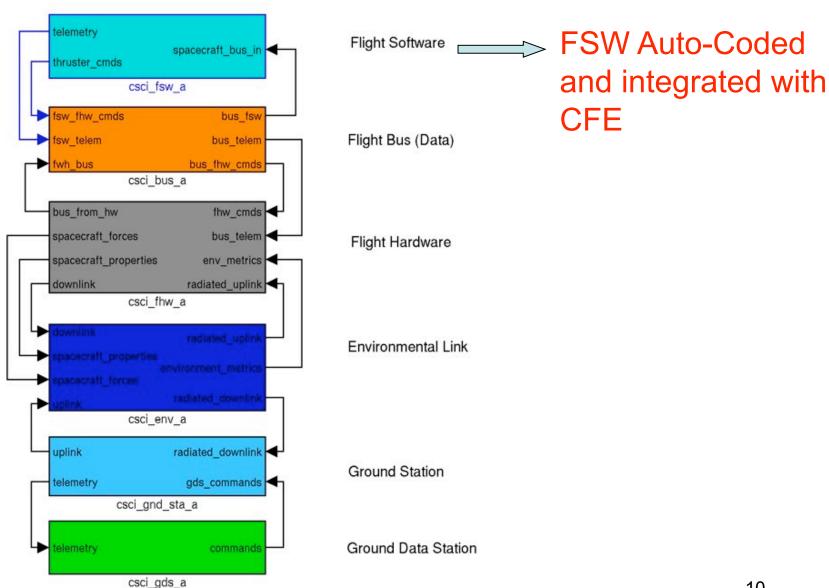
- Simulink supports two way trace-ability between models and generated code
- Code Easy to read, well commented



# **Simulink Model Overview**

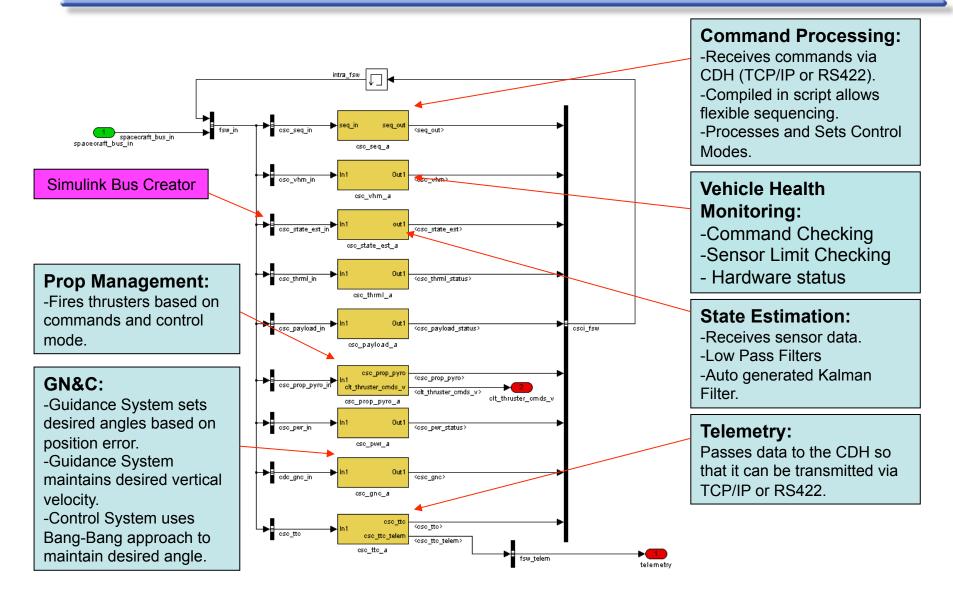


#### **Simulink HTV Architecture**



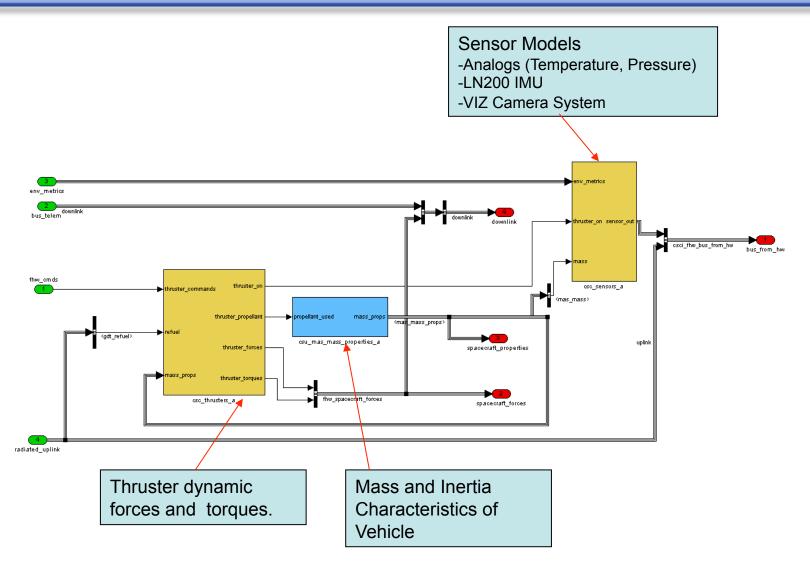


#### Simulink FSW Model



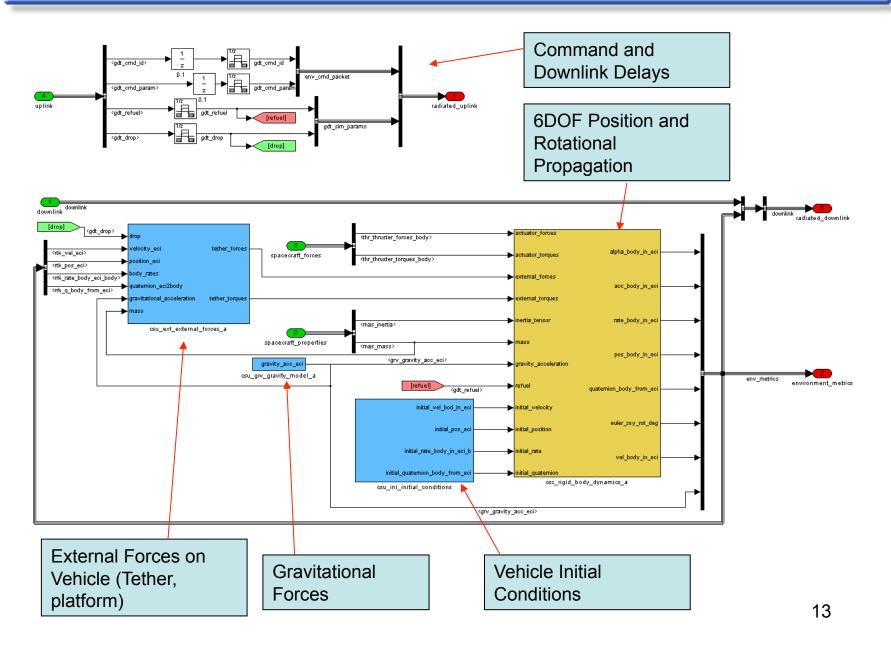


# **Simulink Flight Hardware Model**





#### **Simulink Environment Model**





# **cFE Simulink Integration**



## **cFE – Core Flight Executive**

- Goddard Space Flight Center Developed
- Derived from Legacy Missions
- Flexible infrastructure for Space Flight Software
- Components:
  - Executive Services
  - Event Services
  - Time Services
  - Table Services
  - Software Bus Services

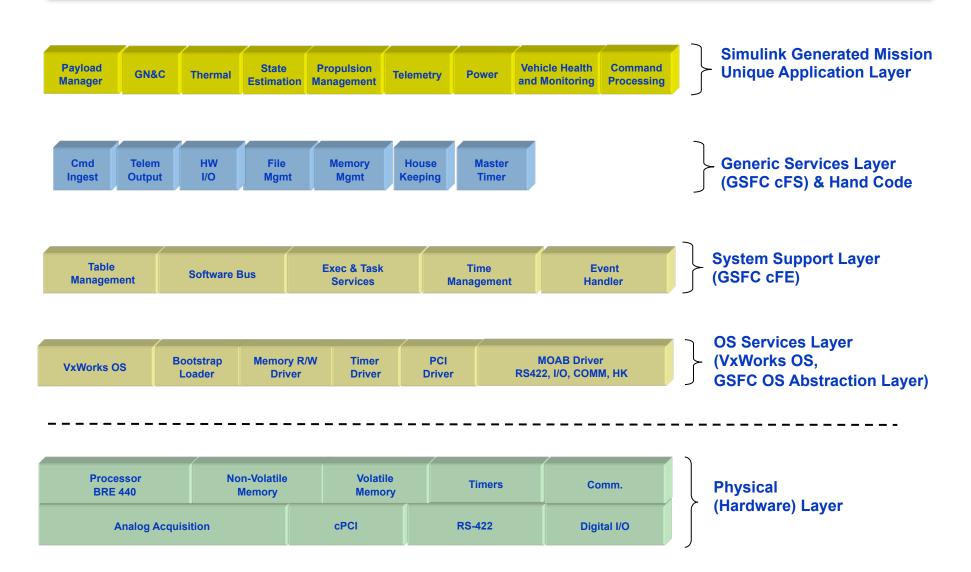


#### **cFE Simulink Development Goals**

- Utilize cFE with no changes
- Automate process during Code Generation.
- Subsystem Blocks generate to cFE Applications that run at desired rates
- Simulink Apps/Blocks Communicate via cFE Software Bus



## **Layered Architecture Approach**





#### cFE Simulink Key Ideas

- Modular Tasks (vs. Monolythic)
  - Pros:
    - More Flexible
    - Simplifies Task Replacement
    - Easier Debugging can look at messages between tasks
  - Cons:
    - Harder to implement
    - More overhead due to more tasks and messages
- Mathworks Template (TLC) File
  - Executed during Code Generation Process
  - Allows customization of created code
  - Leveraged to autocode cFE Apps from Simulink

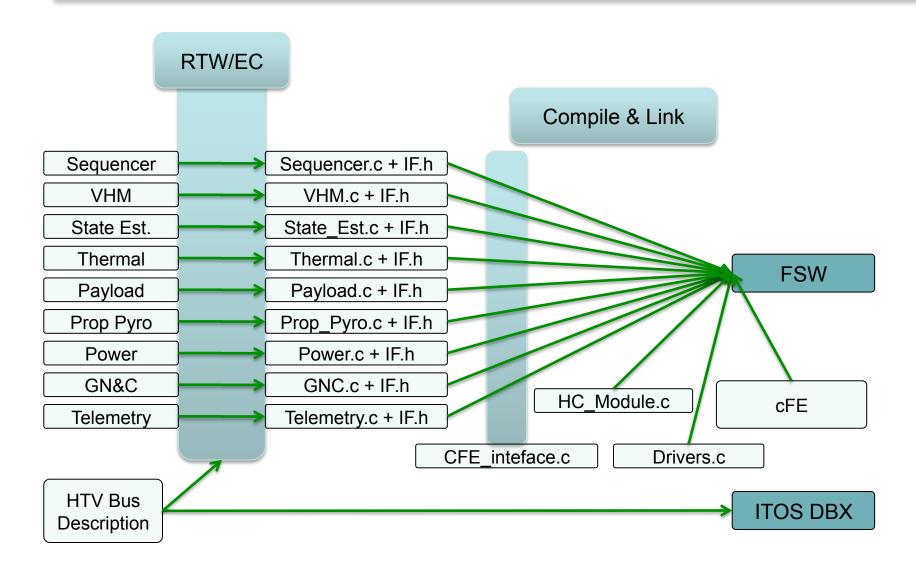


## **cFE Simulink Implementation**

- Simulink Bus translates to cFE Message
- RTW/EC generates Task Description
- Master Timer Generates "Tick" to Schedule Apps and generate Output Messages
- Receive Structure Msgs update local App Input Values
- Apps also Respond to Other Command and Housekeeping Messages

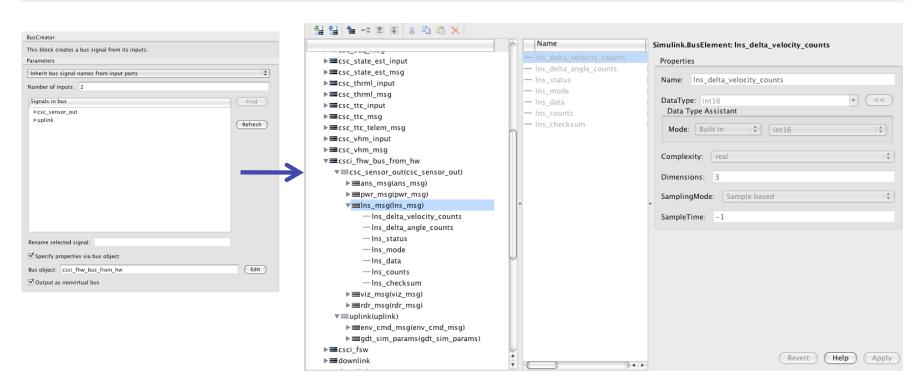


#### **cFE Simulink Autocode Process**





# Simulink Bus becomes cFE Message



```
'Ins_msg', ...

", ...

sprintf("), { ...

{'Ins_delta_velocity_counts', 3, 'int16', -1, 'real', 'Sample'}; ...

{'Ins_delta_angle_counts', 3, 'int16', -1, 'real', 'Sample'}; ...

{'Ins_status', 1, 'int16', -1, 'real', 'Sample'}; ...

{'Ins_mode', 1, 'int16', -1, 'real', 'Sample'}; ...

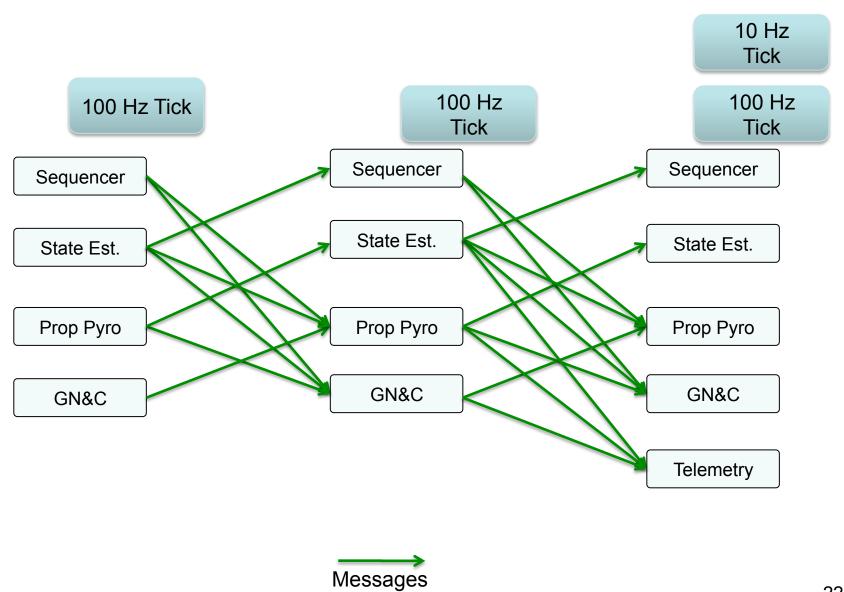
{'Ins_data', 1, 'int16', -1, 'real', 'Sample'}; ...

{'Ins_counts', 3, 'int16', -1, 'real', 'Sample'}; ...

{'Ins_checksum', 1, 'int16', -1, 'real', 'Sample'}; ...
} ...
```



# **cFE Simulink Message Flow**





#### cFE Simulink App Loop

```
Struct App Inputs In
Struct App_Outputs Out
App_Init() {
    Initialize_App_Inputs()
    Subscribe_SB_Msgs(Tick, AppMsgs,...)
    Simulink Init(In, Out)
App_Main(){
    App Init()
    while(1) {
        sb_receive_msg(msg, timeout)
       if (msg == tick) {
              Simulink_Step(dt, In, Out)
              sb send msg(Out) /* app update */
       } else {
              If (msg == app_update) /* Process other App Msgs */
                          App_Update_Inputs(msg, Out)
              else Process Msg(msg) /* HK, Cmds, etc... */
```



#### **New Efforts**

- 3DOF Simulator
- Command Queueing
- Parameter Tables
- Command & Telemetry Dictionary XTCE
- Snapshot/Snapshot Recall
- Latency Reduction
  - Output Message triggers "Step" of Next Module
  - Retains Modularity



#### **Summary**

- NASA Ames developing infrastructure for rapid flight software development
- Model based process leverages Mathworks Simulink, RTW-EC
- Developed modular approach to integrate auto-generated code with GSFC's cFE.
- Successfully demonstrated on HTV
- Being Utilized on NASA's LADEE mission



# **Backup**

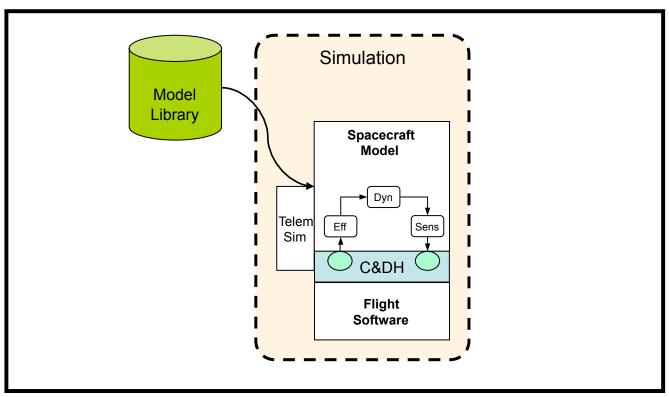


#### cFE IMU App Loop

```
IMU_Main(){
    while(1) {
       struct imu input str imu in
       read_msg_que(imu_in, timeout) /* VxWorks Msg Que */
       sb_send_msg(imu_msg)
       Send_tick()
Cnt = 0;
Send tick() {
    sb send msg(400HZ Tick) /* Do we need 400HZ Tick or key off of IMU Data? */
    if ((Cnt \% 2) == 0) sb_send_msg(200HZ_Tick)
    if ((Cnt \% 4) == 0) sb_send_msg(100HZ_Tick)
    if ((Cnt \% 40) == 0) sb_send_msg(10HZ_Tick)
    if ((Cnt % 400) == 0) sb_send_msg(1HZ_Tick)
    Cnt++;
/* Note: Other Apps same as IMU without the Send tick() */
```



#### **Workstation Simulation**

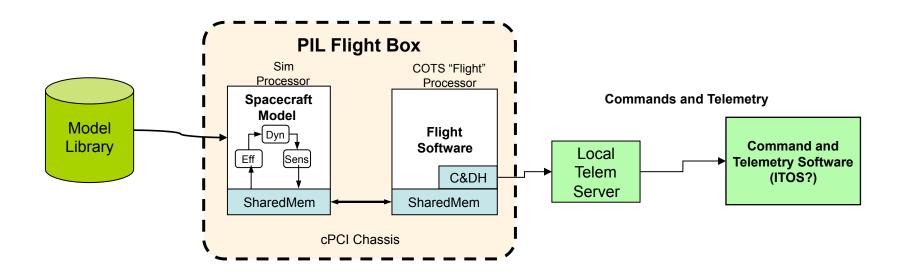


**Local Workstation** 

- •Simulink/SystemBuild Only (No Autocode)
- •Early in development process
- •Algorithm Development
- •Requirements Analysis



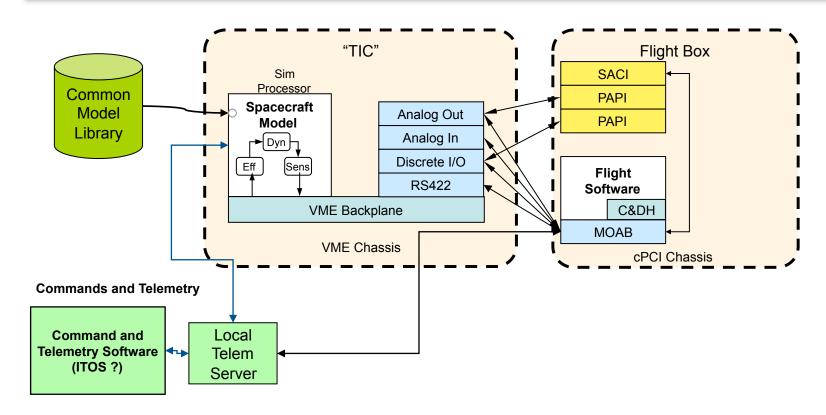
#### **Processor-in-the-Loop Simulation**



- Models autocoded and running on RT processors
- •Inexpensive "flight-like" processor
- •Tests autocoding process & integration with C&DH software
- •Integration with Telemetry Software allows early development/testing of downlink
- •Can be used for initial code size and resource utilization analysis



#### Hardware-in-the-Loop Simulation



- •Flight code runs on Flight Avionics EDU
- •Provides testing of FSW with Avionics I/O
- •Definitive answers on resource utilization
- •Highest fidelity simulations for verification/validation



#### **Motivation for Moving to Simulink**

- Industry appears to be moving that direction.
- Mathworks Extensive support network.
- Mathworks tools for Requirements management, Documentation, and V&V.
- Bus concept makes model management easier.
- Monolithic SystemBuild models not conducive to Reuse and V&V.